

Arquitectura del procesador II

Cronograma 2015

Agosto 2015

D L M M J V S

2 3 4 5 6 7 8

9 10 11 12 13 14 15

16 ~~17~~ 18 *Unidad 1* 20 21 22

~~23~~ 24 ~~25~~ 26 27 28 29
30 31

Septiembre 2015

D L M M J V S

| | | | | | | |
|----|----|----|----|----|----|----|
| | | 1 | 2 | 3 | 4 | 5 |
| 6 | 7 | 8 | 9 | 10 | 11 | 12 |
| 13 | 14 | 15 | 16 | 17 | 18 | 19 |
| 20 | 21 | 22 | 23 | 24 | 25 | 26 |
| 27 | 28 | 29 | 30 | | | |

Unidad 2

Parcial

Octubre 2015

D L M M J V S

Recuperación

1 2 3

4 5 6 7 8 9 10

11 ~~12~~ 13 14 15 16 17

Unidad 3

18 19 20 21 22 23 24

25 26 27 28 29 30 31

Unidad 4

Noviembre 2015

D L M M J V S

1 2 3 4 5 6 7
8 9 10 11 12 13 14

Unidad 4

15 **16** **17** **18** **19** **20** **21**
22 ~~**23**~~ **24** **25** **26** **27** **28**
29 **30**

Parcial

Recuperación

COMPUTER ORGANIZATION AND DESIGN

THE HARDWARE/SOFTWARE INTERFACE

FIFTH EDITION

DAVID A. PATTERSON
JOHN L. HENNESSY



MK
MORGAN KAUFMANN

MICROPROCESSOR ARCHITECTURE

From Simple Pipelines to Chip Multiprocessors



JEAN-LOUP BAER